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PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10042408	01/08/2002	438	110	2813 2812	NEMA BEREZNY

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CONTINUING DATA VERIFIED:
THIS APPLICATION IS A CON OF 10/008,616 10/23/2001 N. Berezny 2813 438
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FOREIGN APPLICATIONS VERIFIED:
JAPAN 6-4816 03/18/1994
JAPAN 6-270,169 11/08/1994
JAPAN 7-7383 01/20/1995

PG-PUB DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed	☐ yes ☐ no	ATTORNEY DOCKET NO
35 USC 119 conditions met	☐ yes ☐ no	7426-082
Verified and Acknowledged Examiner's initials		

TITLE : Fabrication process of semiconductor package and semiconductor package .

U.S. DEPT. OF COMMERCE PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
	Assistant Examiner	Total Claims	Print Claim for O.G.	
ISSUE FEE		**DRAWING**		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
	Primary Examiner	Application Examiner		
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